

# Lecture: An Assembly Language

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## 1 Language

### 1.1 Syntax

*naturals*  $n, pc, i, j, k$

*States*  $s ::= \langle m, R, f, pc \rangle$

*Instructions*  $i ::= \text{add } r_i r_j \mid \text{const } k r_i \mid \text{jmp } r_i \mid \text{jz } r_i$   
 $\mid \text{load } r_i r_j \mid \text{store } r_i r_j \mid \text{cmp } r_i r_j \mid \text{set } r_i r_j$

*Register file*  $R ::= r_1 \mapsto n_1, r_2 \mapsto n_2, \dots$

*Registers*  $r$  taken from an infinite, denumerable set

*Flags*  $f ::= zf = b \quad b = \text{true} \mid \text{false}$

*Memories*  $m ::= n_1 \mapsto m_1, n_2 \mapsto m_2, \dots$

Auxiliaries

$$\begin{array}{c}
 \begin{array}{c} \text{(Memory Access)} \\ \hline \text{pc} \mapsto n \in m \\ \hline m(\text{pc}) = n \end{array} \quad \begin{array}{c} \text{(Register Access)} \\ \hline r \mapsto n \in R \\ \hline R(r) = n \end{array} \\
 \text{(Memory Update)} \\
 \begin{array}{c} m = n_1 \mapsto m_1, n_2 \mapsto m_2, \dots, \text{pc} \mapsto n', \dots \\ m' = n_1 \mapsto m_1, n_2 \mapsto m_2, \dots, \text{pc} \mapsto n, \dots \\ \hline m[\text{pc} \mapsto n] = m' \end{array} \\
 \text{(Register Update)} \quad \text{(Instruction decoding)} \\
 \begin{array}{c} R = r_1 \mapsto m_1, r_2 \mapsto m_2, \dots, r \mapsto n', \dots \\ R' = r_1 \mapsto m_1, r_2 \mapsto m_2, \dots, r \mapsto n, \dots \\ \hline R[r \mapsto n] = R' \end{array} \quad \frac{}{\|i\| = n}
 \end{array}$$

### 1.2 Semantics

$$\begin{array}{c}
 \begin{array}{c} \text{(Eval-add)} \\ \hline m(\text{pc}) = \|\text{add } r_i r_j\| \quad R(r_i) = n_i \quad R(r_j) = n_j \quad n_i + n_j = n_f \\ \hline \langle m, R, f, \text{pc} \rangle \rightarrow \langle m, R[r_i \mapsto n_f], f, \text{pc} + 1 \rangle \end{array} \\
 \begin{array}{c} \text{(Eval-const)} \\ \hline m(\text{pc}) = \|\text{const } j r_i\| \\ \hline \langle m, R, f, \text{pc} \rangle \rightarrow \langle m, R[r_i \mapsto j], f, \text{pc} + 1 \rangle \end{array} \\
 \begin{array}{c} \text{(Eval-jmp)} \\ \hline m(\text{pc}) = \|\text{jmp } r_i\| \quad R(r_i) = n_i \\ \hline \langle m, R, f, \text{pc} \rangle \rightarrow \langle m, R, f, n_i \rangle \end{array} \\
 \begin{array}{c} \text{(Eval-jz-true)} \\ \hline m(\text{pc}) = \|\text{jz } r_i\| \quad R(r_i) = n_i \quad f = zf = \text{true} \\ \hline \langle m, R, f, \text{pc} \rangle \rightarrow \langle m, R, f, n_i \rangle \end{array}
 \end{array}$$

$$\begin{array}{c}
\text{(Eval-jz-false)} \\
\frac{\mathbf{m}(\mathbf{pc}) = \|\mathbf{jz} \ r_i\| \quad \mathbf{R}(r_i) = n_i \quad \mathbf{f} = \mathbf{zf} = \mathbf{false}}{\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow \langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} + 1 \rangle} \\
\text{(Eval-load)} \\
\frac{\mathbf{m}(\mathbf{pc}) = \|\mathbf{load} \ r_i \ r_j\| \quad \mathbf{R}(r_i) = n_i \quad \mathbf{m}(n_i) = k}{\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow \langle \mathbf{m}, \mathbf{R}[r_j \mapsto k], \mathbf{f}, \mathbf{pc} + 1 \rangle} \\
\text{(Eval-store)} \\
\frac{\mathbf{m}(\mathbf{pc}) = \|\mathbf{store} \ r_i \ r_j\| \quad \mathbf{R}(r_i) = n_i \quad \mathbf{R}(r_j) = n_j}{\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow \langle \mathbf{m}[n_j \mapsto n_i], \mathbf{R}, \mathbf{f}, \mathbf{pc} + 1 \rangle} \\
\text{(Eval-cmp)} \\
\frac{\mathbf{m}(\mathbf{pc}) = \|\mathbf{cmp} \ r_i \ r_j\| \quad \mathbf{R}(r_i) = n_i \quad \mathbf{R}(r_j) = n_j \quad \mathbf{f}' = \mathbf{zf} = (n_i == n_j ? \mathbf{true} : \mathbf{false})}{\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow \langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} + 1 \rangle} \\
\text{(Eval-set)} \\
\frac{\mathbf{m}(\mathbf{pc}) = \|\mathbf{set} \ r_i \ r_j\| \quad \mathbf{R}(r_j) = n_j}{\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow \langle \mathbf{m}, \mathbf{R}[r_i \mapsto n_j], \mathbf{f}, \mathbf{pc} + 1 \rangle}
\end{array}$$

The initial state runs execution from address  $\mathbf{0}$  until it encounters an instruction that it cannot decode, the result value in that case is in  $\mathbf{r}_0$ .

## 2 Compiler from the Source to this Target

### 2.1 Compiler Definition

The compiler  $\llbracket \cdot \rrbracket$  takes in input: a source expression  $e$ , a list of registers  $\mathbf{K}$ , a list of bindings  $V$ , an address where to write the instructions. It returns: a list of instructions  $\mathbf{is}$ , a register where the output of that expression can be found  $\mathbf{r}$ , an updated list of registers  $\mathbf{K}'$ , an updated list of bindings  $V$ .

$$\begin{array}{l}
\mathbf{K} ::= \emptyset \mid \mathbf{K}, \mathbf{r} \\
V ::= \emptyset \mid V, \mathbf{x} : \mathbf{r} \\
\|\mathbf{K}\| = n \quad \text{where } \mathbf{K} = \mathbf{r}_1, \dots, \mathbf{r}_n \\
\mathbf{is} = \emptyset \mid \mathbf{is}, \mathbf{i} \\
\|\mathbf{is}\| = n \quad \text{where } \mathbf{is} = \mathbf{i}_1, \dots, \mathbf{i}_n
\end{array}$$

Compiler for whole programs (assuming no instruction decodes to  $\mathbf{0}$ ):

$$\llbracket \mathbf{f}(x) \mapsto e \rrbracket = \mathbf{is}; \mathbf{set} \ \mathbf{r}_0 \ \mathbf{r}_i; \mathbf{0} \quad \text{where } \llbracket e, \emptyset, \mathbf{x} : \mathbf{r}_0, \mathbf{0} \rrbracket = \mathbf{is}, \mathbf{r}_i, \mathbf{K}, V$$

Compiler for partial programs:

$$\llbracket \mathbf{f}(x) \mapsto e \rrbracket = \mathbf{is}; \mathbf{set} \ \mathbf{r}_0 \ \mathbf{r}_i \quad \text{where } \llbracket e, \emptyset, \mathbf{x} : \mathbf{r}_0, \mathbf{100} \rrbracket = \mathbf{is}, \mathbf{r}_i, \mathbf{K}, V$$

Assume the context fills the instruction before address  $\mathbf{100}$  and after address  $\mathbf{100} + \|\mathbf{is}\|$ . We don't really model returns for simplicity

$$\begin{aligned}
\llbracket z, \mathbf{K}, V, \mathbf{a} \rrbracket &= \emptyset, r_i, \mathbf{K}, V && \text{where } z : r_i \in V \\
\llbracket \text{true}, \mathbf{K}, V, \mathbf{a} \rrbracket &= \text{const } 0 \ r_i, r_i, \mathbf{K}, r_i, V && \text{where } i = \|\mathbf{K}\| + 1 \\
\llbracket \text{false}, \mathbf{K}, V, \mathbf{a} \rrbracket &= \text{const } 1 \ r_i, r_i, \mathbf{K}, r_i, V && \text{where } i = \|\mathbf{K}\| + 1 \\
\llbracket n, \mathbf{K}, V, \mathbf{a} \rrbracket &= \text{const } n \ r_i, r_i, \mathbf{K}, r_i, V && \text{where } i = \|\mathbf{K}\| + 1 \\
\llbracket e + e', \mathbf{K}, V, \mathbf{a} \rrbracket &= \text{is}; \text{is}'; \text{add } r_i \ r_j, r_i, \mathbf{K}'', V'' && \text{where } \llbracket e, \mathbf{K}, V, \mathbf{a} \rrbracket = \text{is}, r_i, \mathbf{K}', V' \\
&&& \llbracket e', \mathbf{K}', V', \mathbf{a} + \|\text{is}\| \rrbracket = \text{is}', r_j, \mathbf{K}'', V'' \\
&&& i = \|\mathbf{K}\| + 1 \\
&&& j = \|\mathbf{K}'\| + 1 \\
\llbracket e == e', \mathbf{K}, V, \mathbf{a} \rrbracket &= \text{is}; \text{is}'; \text{cmp } r_i \ r_j && , r_j, \mathbf{K}'', V'' \\
&&& \text{const } k \ r_{i+1}; \text{jz } r_{i+1}; \text{const } 0 \ r_j \\
&&& \text{where } \llbracket e, \mathbf{K}, V, \mathbf{a} \rrbracket = \text{is}, r_i, \mathbf{K}', V' \\
&&& \llbracket e', \mathbf{K}', V', \mathbf{a} + \|\text{is}\| \rrbracket = \text{is}', r_j, \mathbf{K}'', V'' \\
&&& i = \|\mathbf{K}\| + 1 \\
&&& j = \|\mathbf{K}'\| + 1 \\
&&& k = \mathbf{a} + \|\text{is}\| + \|\text{is}''\| + 5 \\
\llbracket \text{if } e \text{ then } e_1 \text{ else } e_2, \mathbf{K}, V, \mathbf{a} \rrbracket &= \begin{cases} \text{is}; \text{const } 0 \ r_{i+1}; \text{cmp } r_i \ r_{i+1}; \text{const } k_1 \ r_{i+1}; \text{jz } r_{i+1} \\ \text{is}''; \text{set } r_i \ r_{i''}; \text{const } k_2 \ r_{i+1}; \text{jmp } r_{i+1} && , r_i, \mathbf{K}''''', V'''' \\ \text{is}'; \text{set } r_i \ r_{i'}; \end{cases} \\
&&& \text{where } \llbracket e, \mathbf{K}, V, \mathbf{a} \rrbracket = \text{is}, r_i, \mathbf{K}', V' \\
&&& \llbracket e_1, \mathbf{K}', V', \mathbf{a}_1 \rrbracket = \text{is}', r_{i'}, \mathbf{K}'', V'' \\
&&& \llbracket e_2, \mathbf{K}', V', \mathbf{a}_2 \rrbracket = \text{is}'', r_{i''}, \mathbf{K}''', V''' \\
&&& k_1 = \mathbf{a} + \|\text{is}\| + 4 + \|\text{is}''\| + 1 \\
&&& k_2 = \mathbf{a} + \|\text{is}\| + 4 + \|\text{is}''\| + 3 + \|\text{is}'\| + 1 \\
&&& \mathbf{a}_1 = \mathbf{a} + \|\text{is}\| + 4 + \|\text{is}''\| + 3 \\
&&& \mathbf{a}_2 = \mathbf{a} + \|\text{is}\| + 4 \\
&&& \mathbf{K}'''' = \max(\mathbf{K}'', \mathbf{K}''') \\
&&& V'''' = \max(V'', V''') \\
\llbracket \text{let } x = e \text{ in } e', \mathbf{K}, V, \mathbf{a} \rrbracket &= \text{is}; \text{is}', r_{i'}, \mathbf{K}'', V'' \\
&&& \text{where } \llbracket e, \mathbf{K}, V, \mathbf{a} \rrbracket = \text{is}, r_i, \mathbf{K}', V' \\
&&& \llbracket e', \mathbf{K}', V', x : r_i, \mathbf{a}_1 \rrbracket = \text{is}', r_{i'}, \mathbf{K}'', V'' \\
&&& \mathbf{a}_1 = \mathbf{a} + \|\text{is}\|
\end{aligned}$$

## 2.2 Compiler Correctness

Let  $\Gamma \vdash \gamma$  say that  $\gamma$  binds the same variables of  $\Gamma$  to values of the right type.

**Lemma 2.1 (Forward simulation).**

if  $\Gamma \vdash e : \tau$  and  $\Gamma \vdash \gamma$  and  $e\gamma \hookrightarrow^* v$  and  $\text{dom}(\mathbf{K}) = \text{img}(V)$   
and  $\llbracket e, \mathbf{K}, V, \mathbf{pc} \rrbracket = \mathbf{is}, \mathbf{ri}, \_ , \_$  and  $e\gamma \sim_V \langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle$   
then  $\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow^{\|\text{is}\|} \langle \mathbf{m}', \mathbf{R}', \mathbf{f}', \mathbf{pc}' \rangle$  and  $v \sim_{\mathbf{ri}} \langle \mathbf{m}', \mathbf{R}', \mathbf{f}', \mathbf{pc}' \rangle$

*Proof.* By induction on the typing derivation of  $e$ .

**2.2.1 Relations for this Proof**

We need a set of cross-language relations.

$$\frac{\begin{array}{c} \text{(State Relation (closed))} \\ \llbracket e, \emptyset, \mathbf{x} : \mathbf{r}_0, \mathbf{pc} \rrbracket = \mathbf{i}_0, \dots, \mathbf{i}_j, \_ , \_ , \_ \\ \forall k \in 0..j \ \mathbf{m}(\mathbf{pc} + \mathbf{k}) \mapsto \mathbf{i}_k \end{array}}{e \sim^c \langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle}$$

A source closed expression is related to a target state if:

- in the memory, starting at address  $\mathbf{pc}$ , there is the list of instructions that result of the compilation of  $e$ ;

$$\frac{\begin{array}{c} \text{(Value Relation)} \\ v \sim^0 \mathbf{R}(\mathbf{r}) \end{array}}{v \sim_{\mathbf{r}} \langle \mathbf{m}', \mathbf{R}', \mathbf{f}', \mathbf{pc}' \rangle} \quad \frac{\text{(Base Value Relation - true)}}{\text{true} \sim^0 \mathbf{0}} \quad \frac{\text{(Base Value Relation - false)}}{\text{false} \sim^0 \mathbf{1}} \quad \frac{\text{(Base Value Relation - n)}}{n \sim^0 \mathbf{n}}$$

A source value is related to a target state at a certain register if:

- in the target state, at the register, there is a number that is in the base relation for values.

$$\frac{\begin{array}{c} \text{(State Relation (ope))} \\ e \sim^c \langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \\ V \vdash \gamma \sim \mathbf{R} \end{array}}{e\gamma \sim_V \langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle} \quad \frac{\text{(Substitution Relation - empty)}}{V \vdash \emptyset \sim \mathbf{R}} \quad \frac{\begin{array}{c} \text{(Substitution Relation - subst)} \\ V(x) = \mathbf{r} \\ v \sim^0 \mathbf{R}(\mathbf{r}) \end{array}}{V \vdash \gamma; [v / x] \sim \mathbf{R}}$$

An open source expression is related to a target state if:

- the expression is related according to the closed expression relation;
- the list of substitutions  $\gamma$  is accounted for in the registers  $\mathbf{R}$ .

### 3 Examples

Compiling this program:

$\llbracket f(x) \mapsto \text{let } z = \text{true in let } y = 2 \text{ in if } z \text{ then } 0 \text{ else } y + 3 \rrbracket$

results in this assembly:

100 const 0 r <sub>1</sub>	z = true
101 const 2 r <sub>2</sub>	y = 2
102 const 0 r <sub>3</sub>	if loads true
103 cmp r <sub>1</sub> r <sub>3</sub>	if checks z == true
104 const 111 r <sub>3</sub>	distance to then
105 jz r <sub>3</sub>	if
106 const 3 r <sub>4</sub>	else: 3
107 add r <sub>2</sub> r <sub>4</sub>	y + 3
108 set r <sub>3</sub> r <sub>2</sub>	set to if-result register
109 const 113 r <sub>4</sub>	end offset
110 jmp r <sub>4</sub>	goto end
111 const 0 r <sub>4</sub>	then: 0
112 set r <sub>3</sub> r <sub>4</sub>	set to if-result register
113 set r <sub>0</sub> r <sub>3</sub>	set to program result register

Compiling this program:

$\llbracket f(x) \mapsto x \rrbracket$

results in this assembly:

100 set r <sub>0</sub> r <sub>0</sub>	x
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Compiling this program:

$\llbracket f(x) \mapsto \text{if } x \text{ then } 0 \text{ else } 1 \rrbracket$

results in this assembly:

100 const 0 r <sub>1</sub>	if loads true
101 cmp r <sub>0</sub> r <sub>1</sub>	if checks x == true
102 const 108 r <sub>1</sub>	distance to then
103 jz r <sub>1</sub>	if
104 const 1 r <sub>2</sub>	else: 1
105 set r <sub>0</sub> r <sub>2</sub>	set to if-result register
106 const 110 r <sub>1</sub>	end offset
107 jmp r <sub>1</sub>	goto end

108 const 0 r <sub>3</sub>	then: 0
109 set r <sub>0</sub> r <sub>3</sub>	set to if-result register
110 set r <sub>0</sub> r <sub>0</sub>	set to program result register

## 4 Supporting Fully-Abstract Compilation

In order to support fully-abstract compilation we extend the language with a primitive that zeroes-out all registers except one.

*Instructions*  $i ::= \dots \mid \mathbf{zero\ r}$

$$\frac{\mathbf{m(pc)} = \|\mathbf{zero\ r}\| \quad \mathbf{R(r)} = \mathbf{n} \quad \forall i \geq 0. \mathbf{R}' = \mathbf{r}_i \mapsto \mathbf{0}}{\langle \mathbf{m}, \mathbf{R}, \mathbf{f}, \mathbf{pc} \rangle \rightarrow \langle \mathbf{m}, \mathbf{R}'[\mathbf{r} \mapsto \mathbf{n}], \mathbf{f}, \mathbf{pc} + 1 \rangle} \text{ (Eval-zero)}$$

Fully-abstract compiler for partial programs:

$$\llbracket \mathbf{f}(x) \mapsto \mathbf{e} \rrbracket = \mathbf{is; set\ r_0\ r_i; zero\ r_0} \quad \text{where } \llbracket \mathbf{e}, \emptyset, x : \mathbf{r_0}, \mathbf{100} \rrbracket = \mathbf{is, r_i, K, V}$$

Note that we avoid many issues by insisting that all source functions are typed at  $\mathbf{Nat} \rightarrow \mathbf{Nat}$ . Any change in any of those types would require the introduction of a typecheck in order for the compiler to be fully-abstract.